



IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In the application of:

Yasuaki HIRANO

Serial No.: 09/598,384

Filing Date: June 21, 2000

For: ERASE METHOD FOR
NONVOLATILE SEMICONDUCTOR
STORAGE DEVICE AND ROW...

Examiner: T Phan

Group Art Unit: 2818

SUBMISSION OF FORMAL DRAWINGS

U.S. Patent and Trademark Office
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Sir:

Enclosed are five (5) sheets of formal drawings containing Figs. 11A-11M, 12, 13, 14, 19, 20 and 21 in connection with the above-identified application.

Respectfully submitted,

Dated: January 28, 2004

By:

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